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Art Unit

2812

(Use as many sheets as necessary) Examiner Name J. Kennedy MI22-2358 Sheet 1 Attorney Docket Number ot 2 U. S. PATENT DOCUMENTS **Document Number** Publication Date Name of Patentas or Pages, Columna, Lines, Where Relevant Passages or Relevant Figures Appear Applicant of Cited Document Number-Kind Code<sup>2</sup> P manual US 5,532,107 7/1998 Ole at at US. 2001/0006752 7/2001 Watanabe et al. US US-119 US-US-211 US US-US-UR US

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			Art Unit	2812	
			Examiner Name	J. Kennedy	
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
gu		Wolf et al., Silicon Processing for the VLSI Era, Volume 1-Process Technology, Second Edition, Lattice Press, 2000, pgs. 515-517.	
gu		Wolf et al., Silicon Processing for the VLSI Era, Volume 1-Process Technology, Second Edition, Lattice Press, 2000, pgs. 527-531.	
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